

Amendments to the Drawings

Filed herewith are corrected drawing sheets for Figs. 3, 4 and 6. It is respectfully requested that the new drawing sheets be substituted for the originally filed drawing sheets for Figs. 3, 4 and 6. Extra copies of the original drawing sheets for Figs. 3, 4 and 6, with changes indicated in red, are also filed herewith.

The diagram shows a circuit block 401 labeled "Band gap Voltage Reference Generator". Inside this block, there is an operational amplifier with its non-inverting input (+) connected to a resistor network. The inverting input (-) is connected to a resistor network and the output. The output of the op-amp is labeled V_{REF} . Outside the block 401, the V_{REF} signal is connected to a voltage divider consisting of two resistors. The output of this divider is labeled 403 and is connected to the "Internal Supply Voltage". A label 402 points to the op-amp in the first stage.

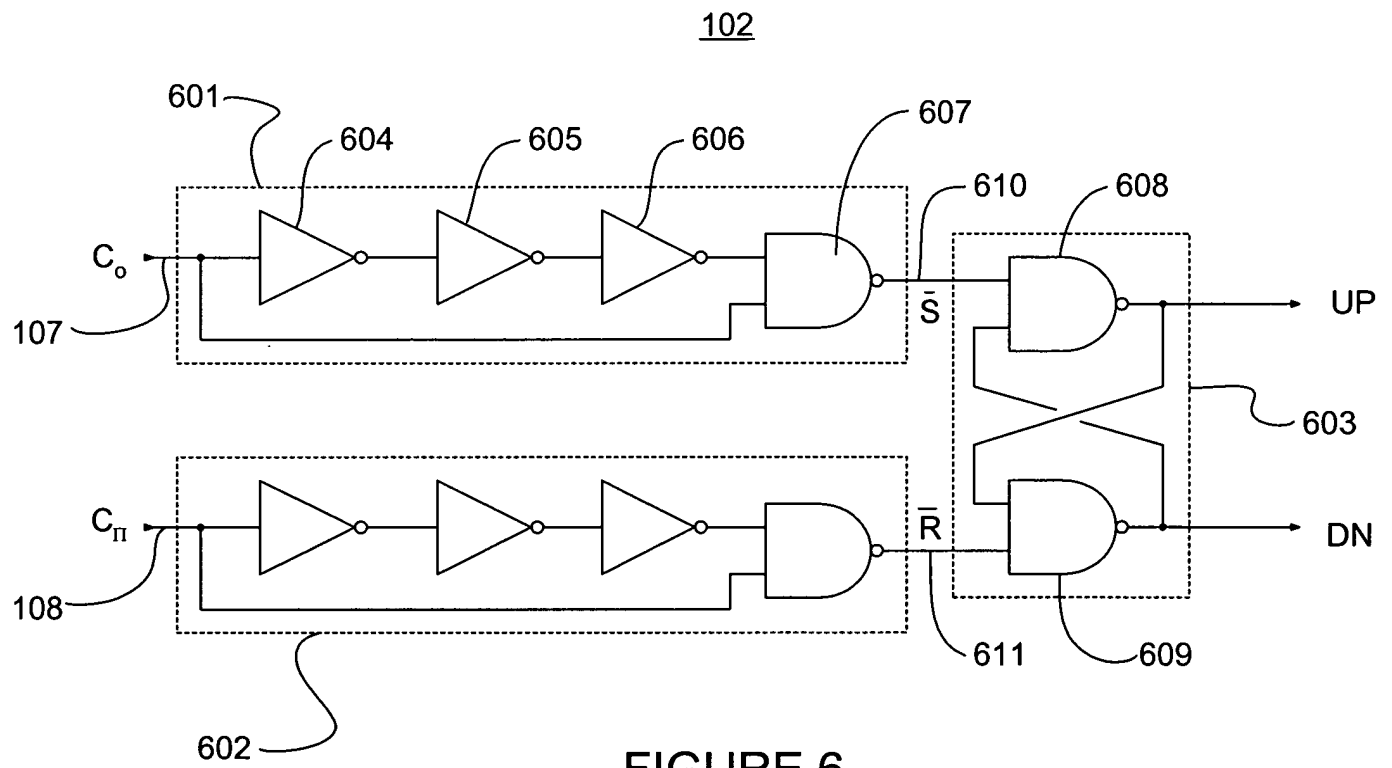


FIGURE 6